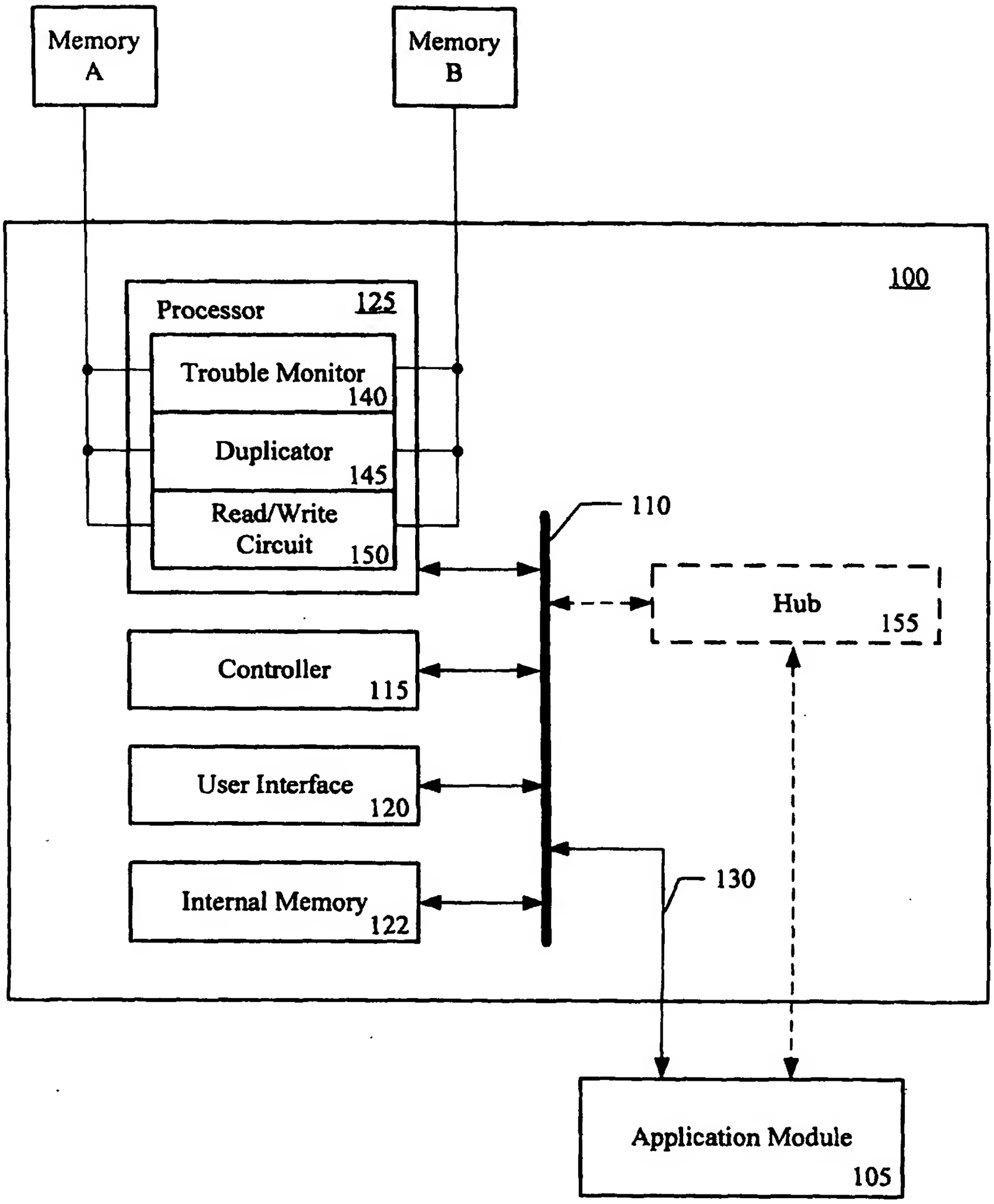
Memory A
Memory B
100
Processor 125
Trouble Monitor 140
Duplicator 145
Read/Write Circuit 150
110
Hub 155
Controller 115
User Interface 120
Internal Memory 122
130
Application Module 105

Figure 1

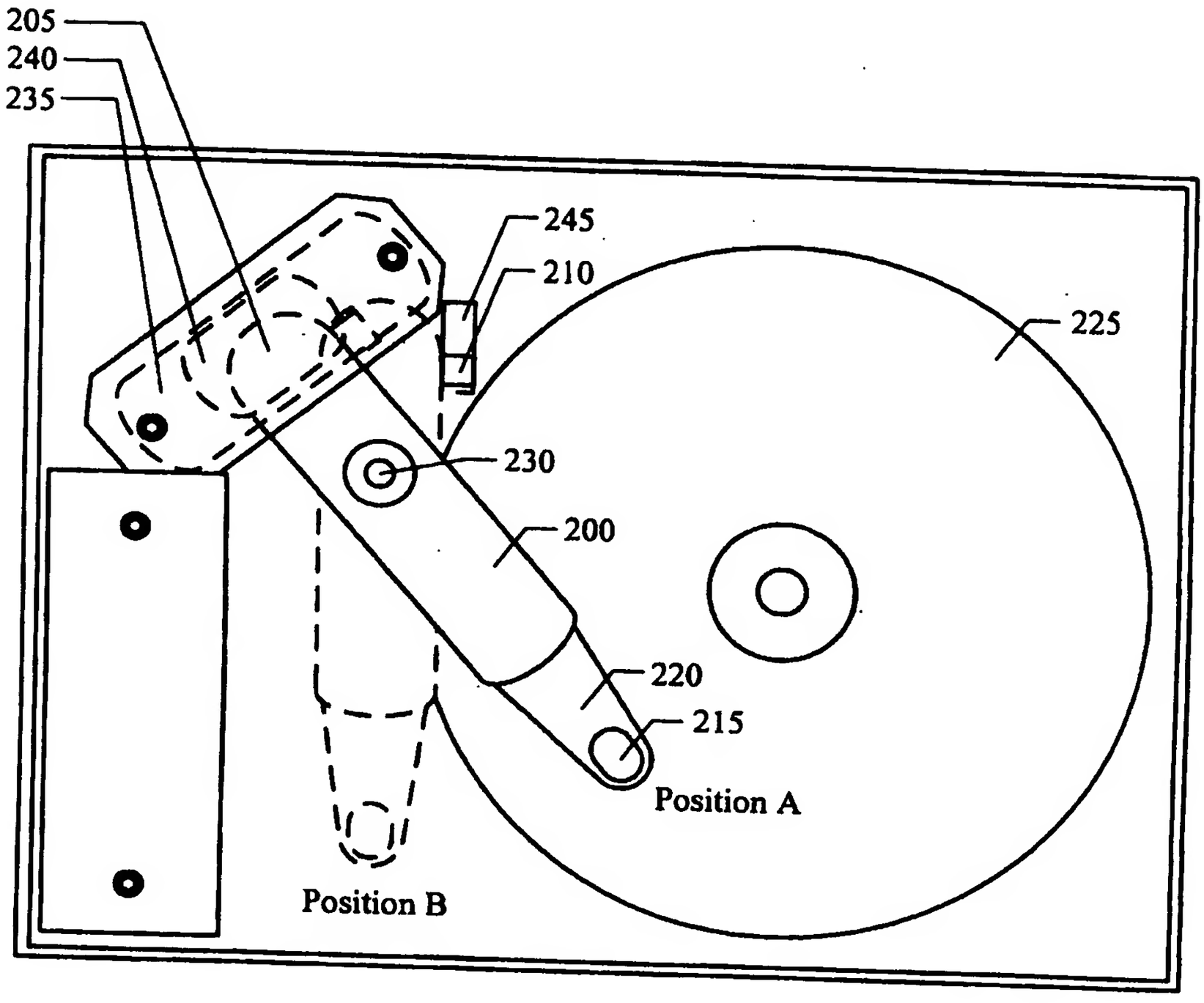

Figure 2

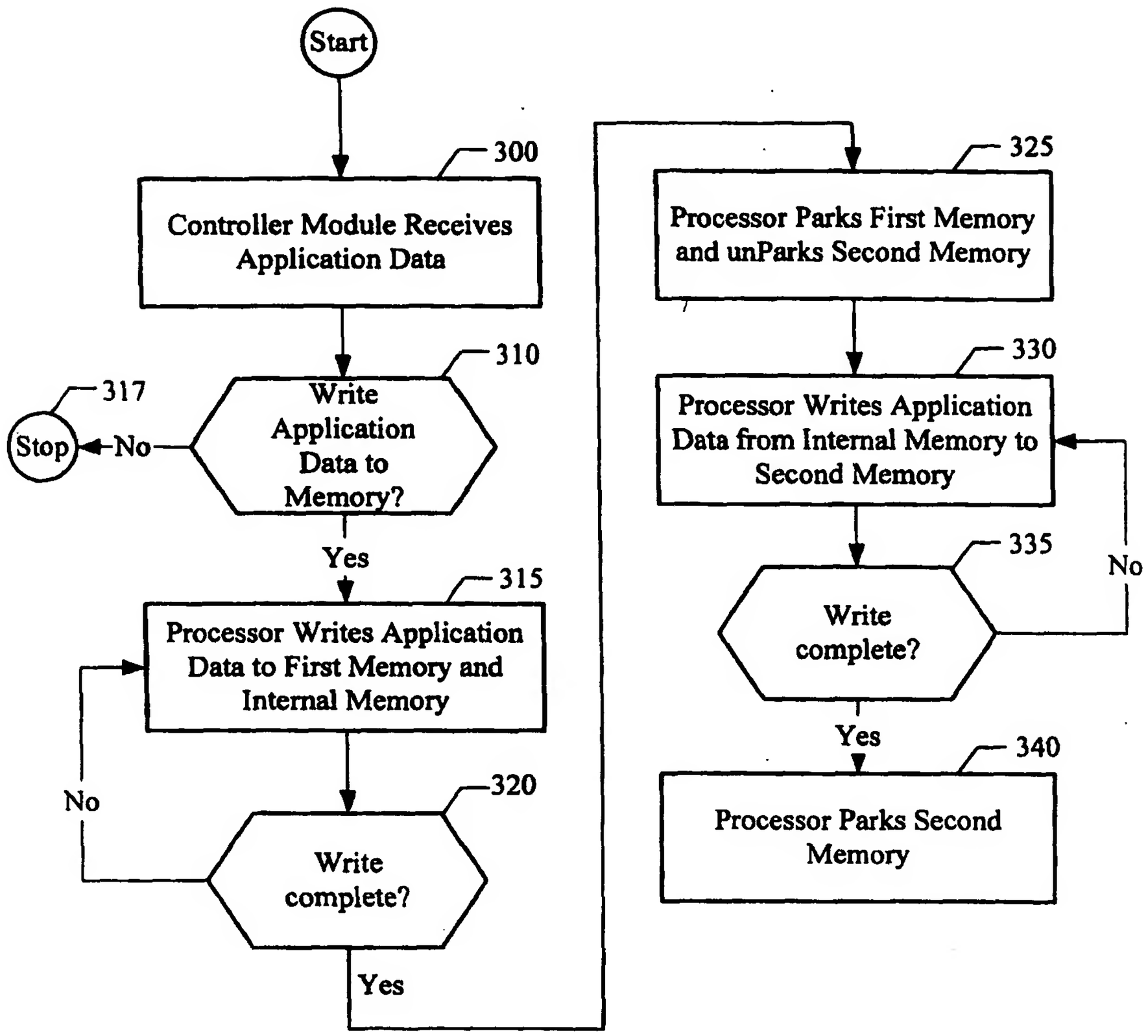
Start
300
Controller Module Receives Application Data
310
Write Application Data to Memory?
317
Stop
No
Yes
315
Processor Writes Application Data to First Memory and Internal Memory
320
Write complete?
No
Yes
325
Processor Parks First Memory and unParks Second Memory
330
Processor Writes Application Data from Internal Memory to Second Memory
335
Write complete?
No
Yes
340
Processor Parks Second Memory

Figure 3

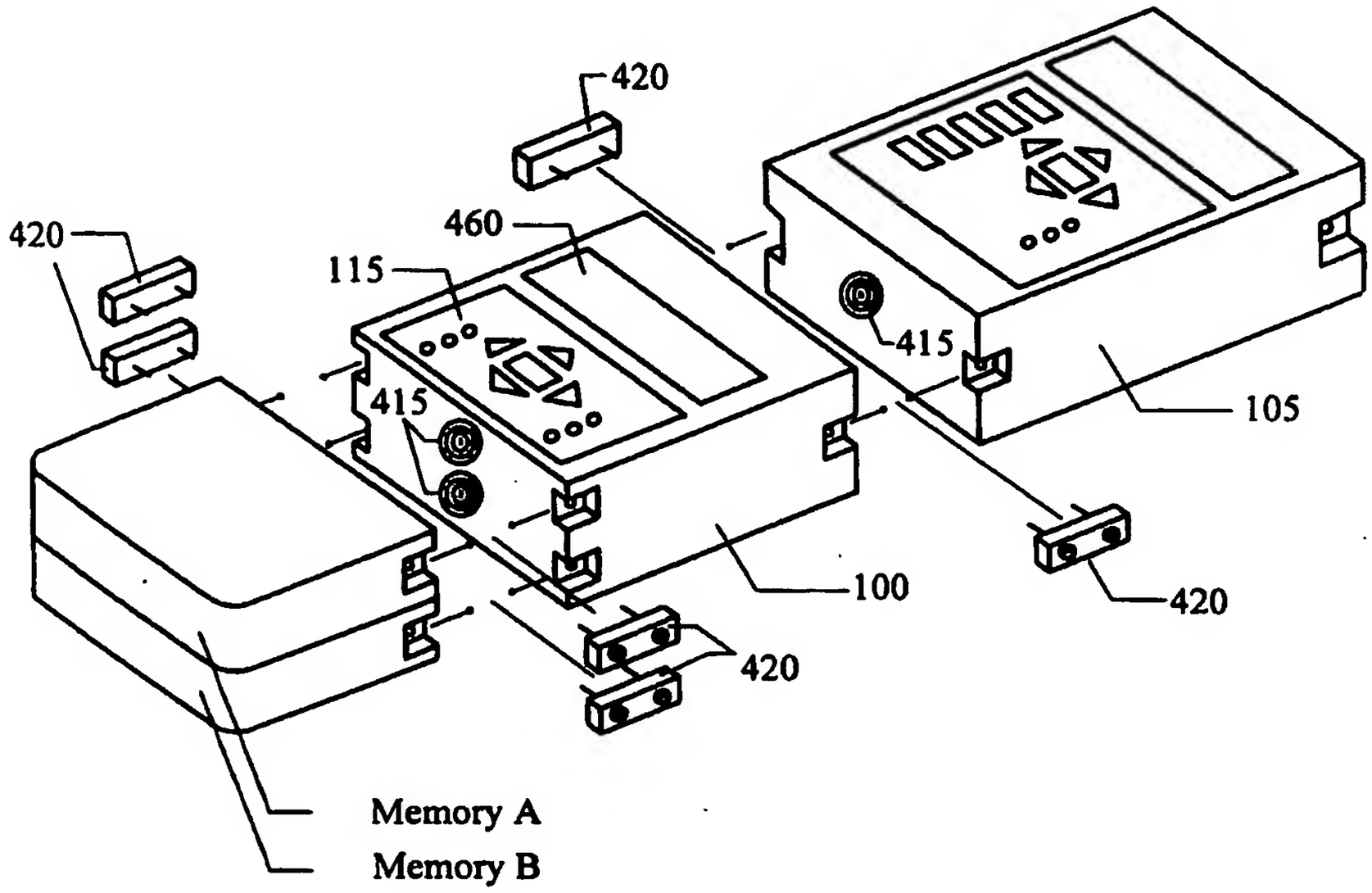

Figure 4

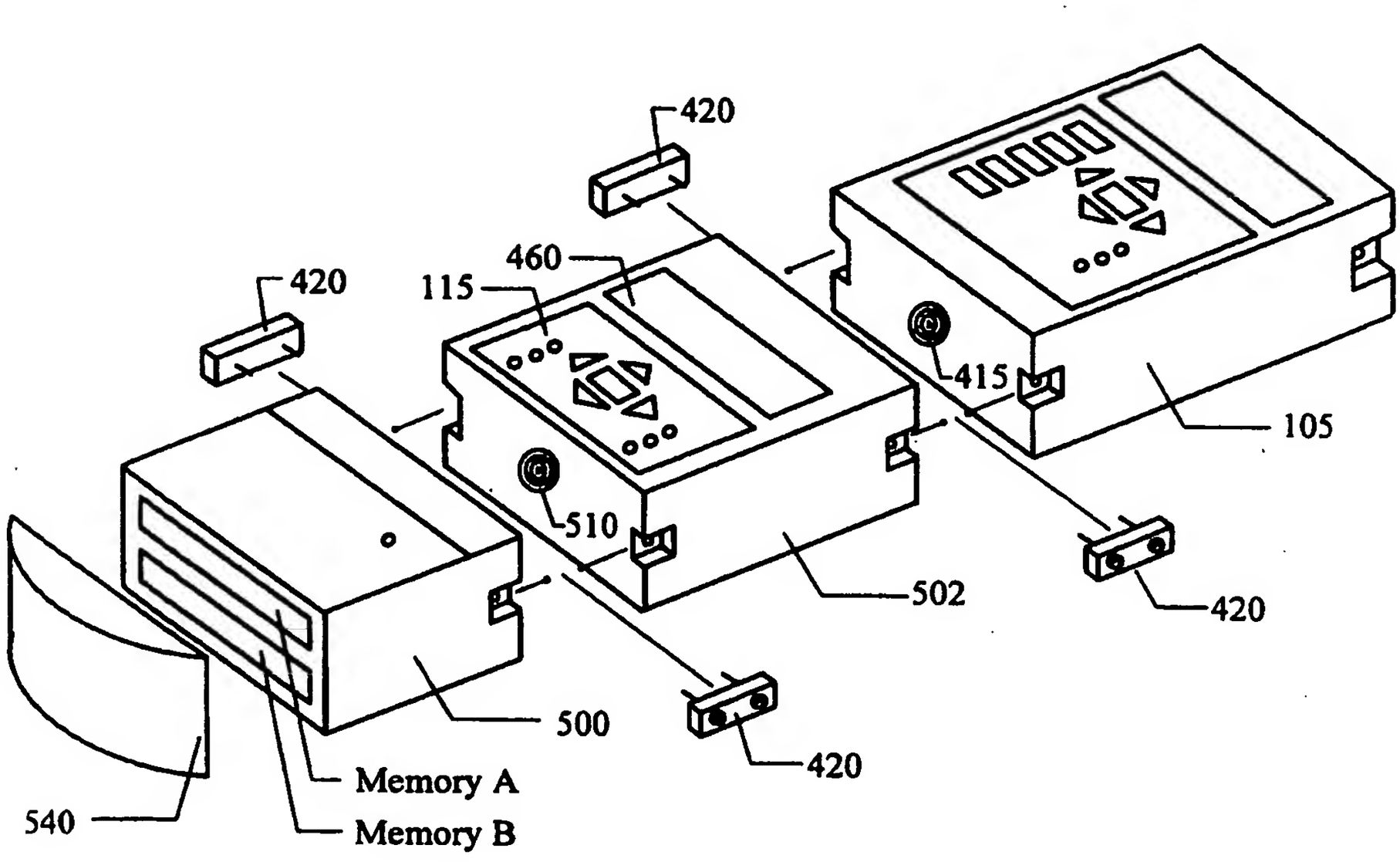
420
460
115
420
415
105
510
502
420
500
420
Memory A
Memory B
540

Figure 5

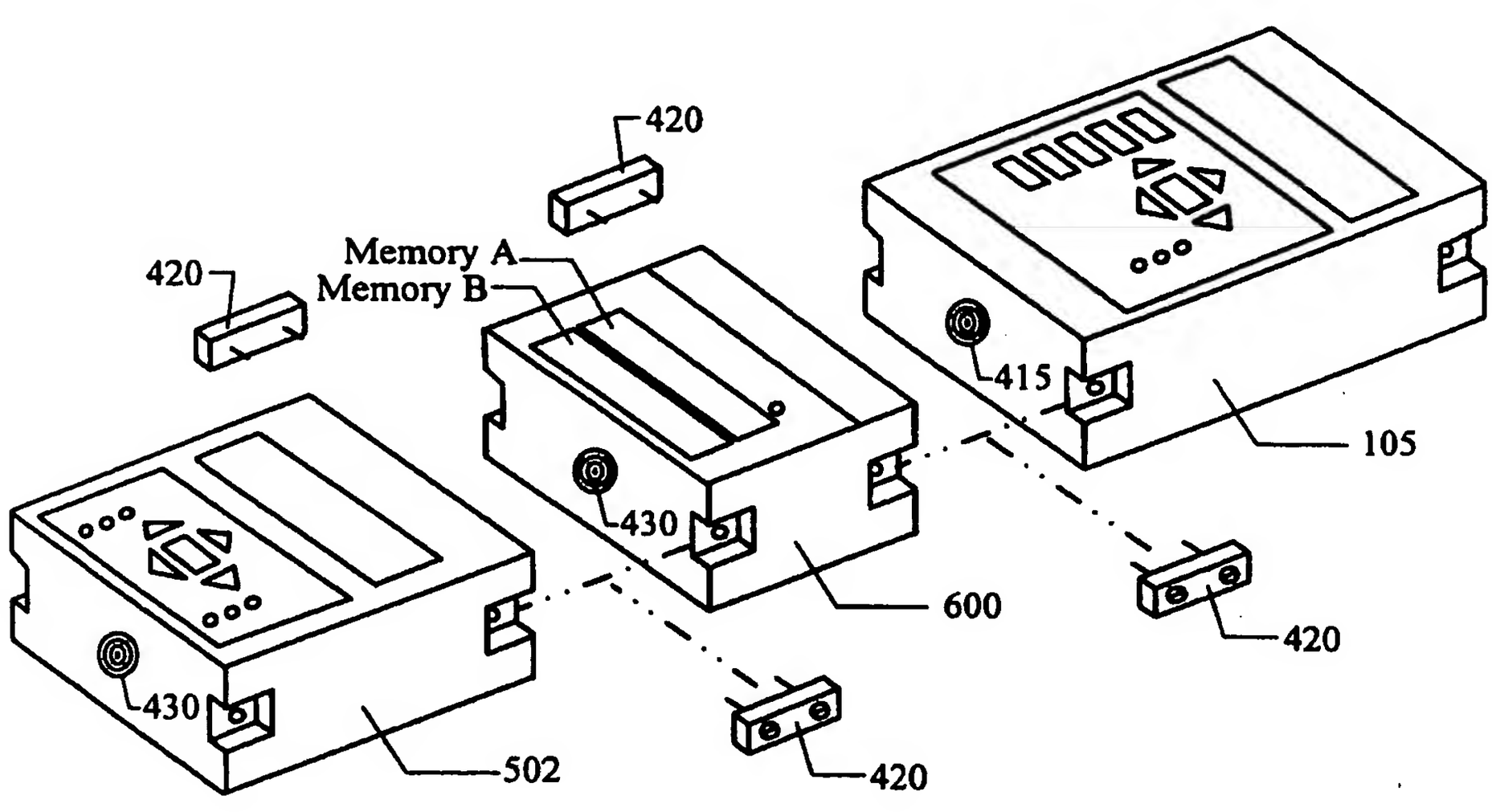

Figure 6